

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Atsufumi SHIBAYAMA, et al.

Title:

DATA PROCESSOR WITH AN IMPROVED

DATA DEPENDENCE DETECTOR

Appl. No.:

09/994,806

Filing Date:

11/28/2001

Examiner:

Unknown

Art Unit:

2152

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents Washington, D.C. 20231

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith are the formal drawings (12 sheets, Figures 1A-1D, 2-5, 6A-6C, 7A-7C, 8, 9A-9C, and 10) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date March 3, 2003

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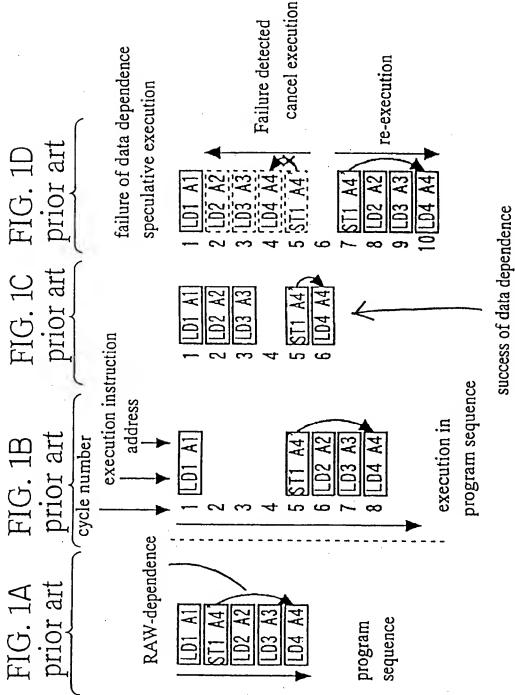
Baron C. Cualte

Registration No. 26,257

DETECTOR

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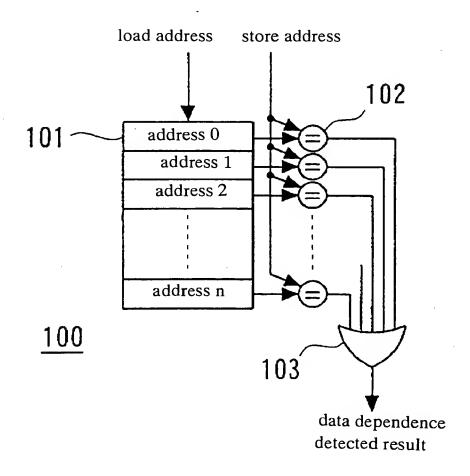
speculative execution



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FIG. 2 prior art

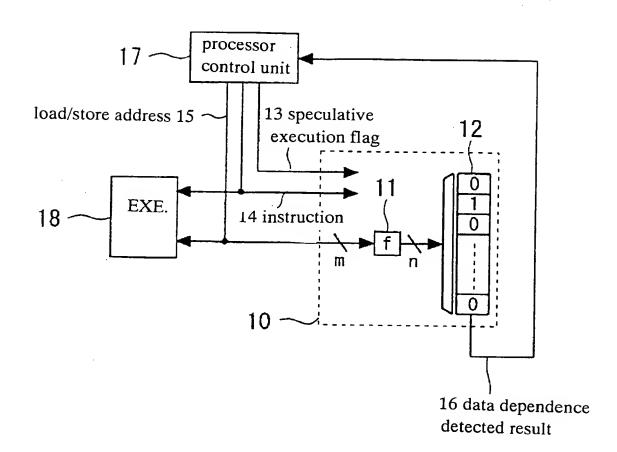




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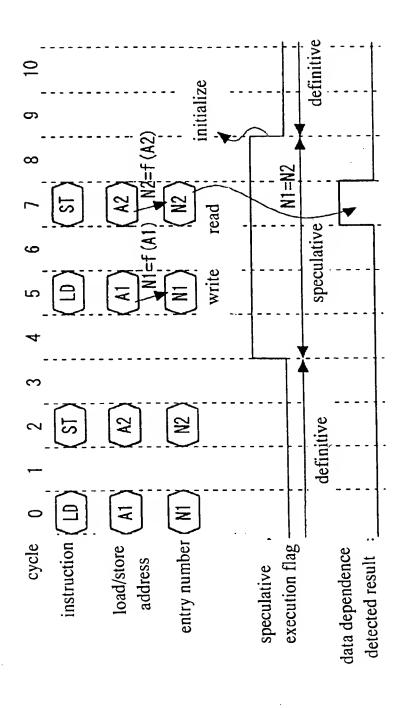
FIG. 3





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FIG. 4

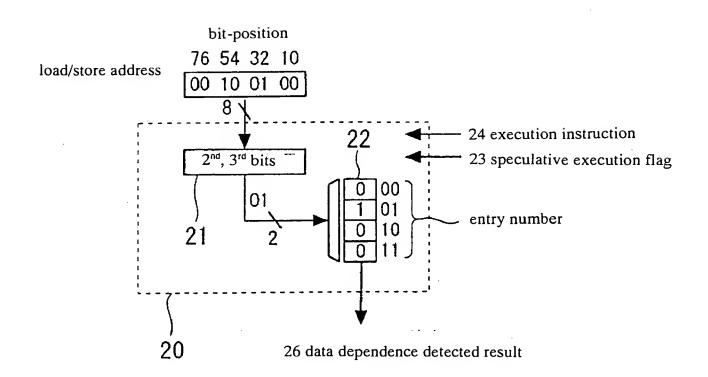




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FIG. 5



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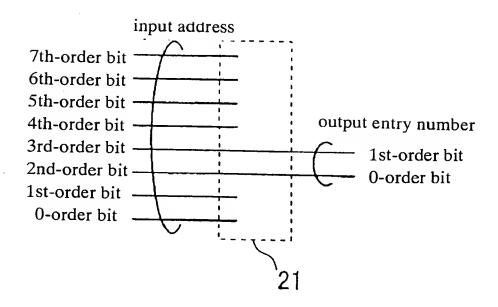
failure of speculative exe. initializing history table 00 entry number load history table speculative execution flag entry number referred 10 01 data dependence FIG. 6C detection cycle program sequence FIG. 6B address execution in instruction cycle number FIG. 6A RAW-dependence program sequence



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FIG. 7A

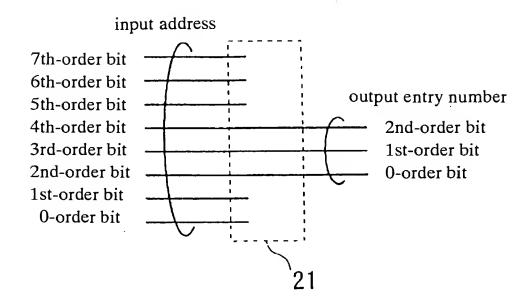




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FIG. 7B

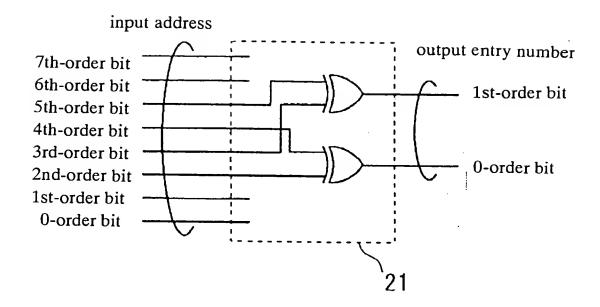




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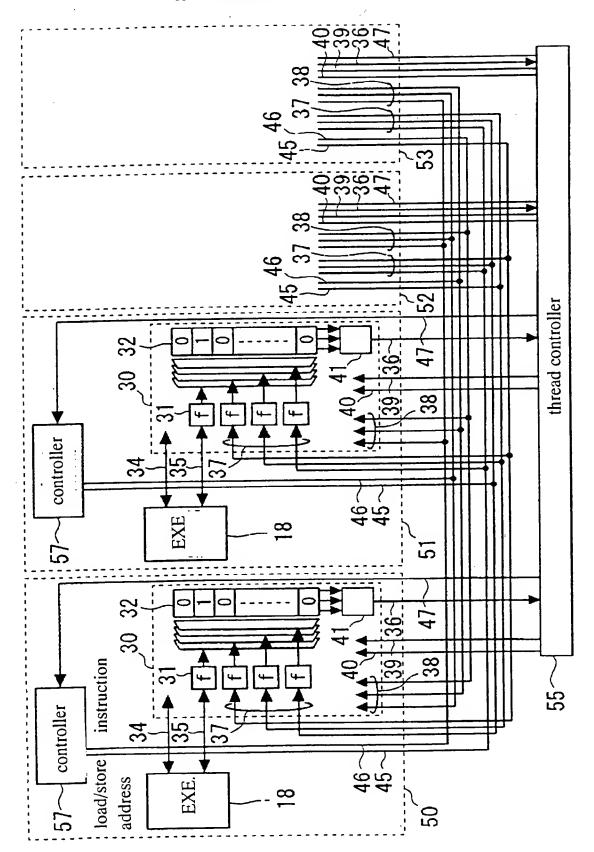
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FIG. 7C



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Title: DATA PROCESSOR WITH AN IMPROVED DATA DEPENDENCE **DETECTOR** Inventors: Atsufumi SHIBAYAMA, et al. Appl. No.: 09/994,806 failure of speculative exe. initializing history table 00 entry number detection in processor 50 data dependence FIG. 9C load history table 11 10 01 cycle number processor 50 processor 51 processor 52 processor 53 thread 3 thread 0 FIG. 9B thread 1 address cycle number i instruction thread 2 RAW-dependence FIG. 9A thread 0 thread 24 thread 3. thread 1

execution sequence

program sequence

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FIG. 10

